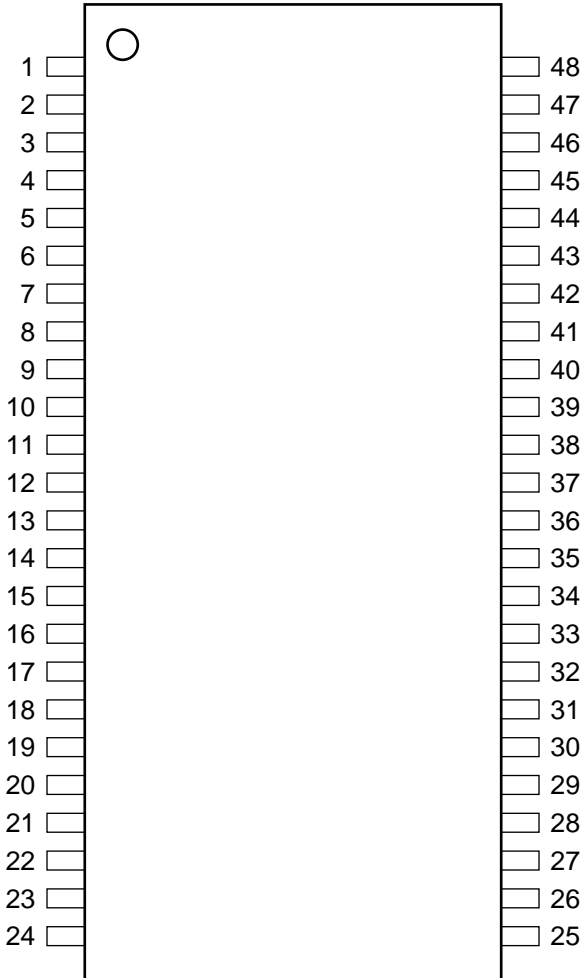


16-BIT TRANSPARENT LATCH

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	$\overline{OE1}$	25	I	LE2
2	O	O0	26	I	I15
3	O	O1	27	I	I14
4	—	GND	28	—	GND
5	O	O2	29	I	I13
6	O	O3	30	I	I12
7	—	Vcc	31	—	Vcc
8	O	O4	32	I	I11
9	O	O5	33	I	I10
10	—	GND	34	—	GND
11	O	O6	35	I	I9
12	O	O7	36	I	I8
13	O	O8	37	I	I7
14	O	O9	38	I	I6
15	—	GND	39	—	GND
16	O	O10	40	I	I5
17	O	O11	41	I	I4
18	—	Vcc	42	—	Vcc
19	O	O12	43	I	I3
20	O	O13	44	I	I2
21	—	GND	45	—	GND
22	O	O14	46	I	I1
23	O	O15	47	I	I0
24	I	$\overline{OE2}$	48	I	LE1

PIN DESCRIPTION

PIN NAMES	DESCRIPTION
LE	LATCH ENABLE INPUTS
\overline{OE}	OUTPUT ENABLE INPUTS (ACTIVE LOW)
I0 - I15	INPUTS
O0 - O15	OUTPUTS

FUNCTION TABLE

INPUTS			OUTPUTS
I0 - I7	LE1	$\overline{OE1}$	O0 - O7
x	x	1	HI-Z
0	1	0	0
1	1	0	1
x	0	0	O0

INPUTS			OUTPUTS
I8 - I15	LE2	$\overline{OE2}$	O8 - O15
x	x	1	HI-Z
0	1	0	0
1	1	0	1
x	0	0	O0

0 : LOW LEVEL

1 : HIGH LEVEL

x : DON'T CARE

HI-Z : HIGH IMPEDANCE

O0 : PREVIOUS O0 BEFORE HIGH TO LOW TRANSITION LATCH ENABLE

